

Product Search Data Sheet

Please download the latest datasheet of BLM18KG260TN1# from the official website of Murata Manufacturing Co., Ltd.

http://www.murata.com/en/products/productdetail?partno=BLM18KG260TN1%23

Note: This datasheet may be out of date.

"#" indicates a package specification code.

BLM18KG260TN1#







< List of part numbers with package codes >

BLM18KG260TN1B

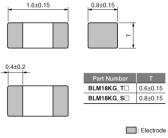
BLM18KG260TN1D

BLM18KG260TN1J



Appearance & Shape









Applications

Other Usage	For general
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Packaging Information

Packaging	Specifications	Standard Packing Quantity
В	Bulk(Bag)	1000
D	180mm Paper Tape	4000
J	330mm Paper Tape	10000



Features

The chip ferrite beads BLM series is designed to function nearly as a resistor at noise frequencies, which greatly reduces the possibility of resonance and leaves signal wave forms undistorted.

BLM series is effective in circuits without stable ground lines because BLM series does not need a connection to ground.

The nickel barrier structure of the external electrodes provides excellent solder heat resistance. BLM_K series can be used in high current circuits due to its low DC resistance. It can match power lines to a maximum of 6ADC.

1 of 3

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- 2. This datasheet has only typical specifications because there is no space for detailed specifications
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Specifications

Shape	SMD
Size Code (in inch)	0603
Length	1.6mm
Length Tolerance	±0.15mm
Width	0.8mm
Width Tolerance	±0.15mm
Thickness	0.6mm
Thickness Tolerance	±0.15mm
Operating Temperature Range	-55°C to 125°C
Mass(typ.)	0.004g
Number of Circuit	1
Rated Current (at 85°C)	6A
Rated Current (at 125°C)	4A
DC Resistance(max.)	0.007Ω
Impedance (at 100MHz)	26Ω
Impedance (at 100MHz) Tolerance	±25%
Size Code (in mm)	1608

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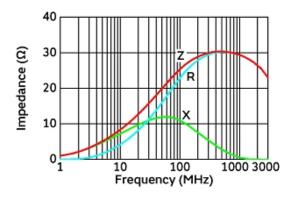
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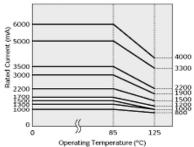


BLM18KG260TN1#



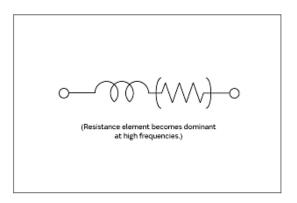
In operating temperature exceeding +85°C, derating of current is necessary for BLM18KG series. Please apply the derating curve shown in chart according to the operating temperature.

Derating of Rated Current



Impedance-Frequency Characteristics

Derating of Rated Current



Equivalent Circuit

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